



Information Disclosure Statement
USSN 09/696,826
March 8, 2004
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Form PTO-1449 (Modified)	ATTY DOCKET NO. B-3650 617089-5	U.S. SERIAL NO. 09/696,826
LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANTS James P Baukus, et al.	
	FILING DATE October 25, 2000	GROUP 2811

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUB- CLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
TT	09/882,892		Baukus et al.	—	—	06/15/2001
TT	10/132,523		Clark Jr. et al.	—	—	04/24/2003
TT	10/735,841		Chow et al.	—	—	12/12/2003

FOREIGN PATENT DOCUMENTS

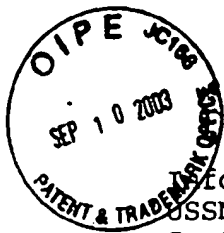
DOCUMENT NUMBER	ISSUE DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER <i>Wain</i>	DATE CONSIDERED 05-12-2004
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LIST OF PATENTS AND PUBLICATIONS STATEMENT	APPLICANT(S) James P. Baukus, et al.	
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
TT	4,101,344	7/1978	Kooi et al.	148	1.5	
TT	4,322,736	3/1982	Sasaki et al.	257	369	
TT	5,177,589	1/1993	Kobayashi et al.	257	773	
TT	5,225,699	7/1993	Nakamura	257	306	
TT	5,317,197	5/1994	roberts	257	401	
TT	5,345,105	9/1994	Sun et al.	257	659	
TT	5,404,040	4/1995	Hshieh et al.	257	341	
TT	5,412,237	5/1995	Komori et al.	257	306	
TT	5,677,557	10/1997	Wuu et al.	257	382	
TT	5,821,590	10/1998	Lee et al.	257	377	
TT	5,854,510	12/1998	Sur, Jr. et al.	257	529	
TT	5,888,887	3/1999	Li et al.	438	525	
TT	5,930,667	7/1999	Oda	438	622	
TT	5,998,257	12/1999	Lane et al.	438	253	
TT	6,037,627	3/2000	Kitamura et al.	257	324	
TT	6,080,614	6/2000	Neilson et al.	438	238	
TT	6,137,318	10/2000	Takaaki	326	112	
TT	6,503,787 B1	1/2003	Choi	438	214	

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FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
TT	0 186 855 A2	7/1986	EP	—	—	
TT	0 920 057 A2	6/1999	EP	—	—	
TT	2486717	1/1982	FR	—	—	
TT	61-147551	7/1986	JP	—	—	abstract
TT	10-256398	9/1998	JP	—	—	abstract
TT	98/57373	12/1998	WO	—	—	abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

TT	Blythe, et al., "Layout Reconstruction of Complex Silicon Chips," <i>IEEE Journal of Solid-State Circuits</i> , pp. 138-145 (February 1993).
TT	Ng, K.K., <i>Complete Guide to Semiconductor Devices</i> , McGraw-Hill, Inc., pp 164-165 (1995).
TT	Sze, S.M., ed., "Silicides for Gates and Interconnections," <i>VLSI Technology</i> , McGraw-Hill, pp. 372-80 (1983).
TT	IBM_TDB, "Double Polysilicon Dynamic Memory Cell with Polysilicon Bit Line," pp 3828-3831 (February 1979).
TT	IBM_TDB, "Static Ram Double Polysilicon Process," pp 3683-3686 (January 1981).

EXAMINER	DATE CONSIDERED
<i>Thien Tme</i>	05-12-04

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Information Disclosure Statement
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	FILING DATE October 25, 2000	GROUP 2811

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE or 102(e) DATE IF APPROPRIATE
TT	5,895,241	4/1999	Lu et al.	438	275	

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	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO
TT	0 186 855 A2	7/1986	EP	—	—	
TT	1 193 758 A1	4/2002	EP	—	—	
TT	1 202 353 A1	5/2002	EP	—	—	
TT	61-147551	7/1986	JP	—	—	abstract
TT	98/57373	12/1998	WO	—	—	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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EXAMINER <i>Thien</i>	DATE CONSIDERED 05-12-2004
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